

IFW 2123

LAHIVE & COCKFIELD
L L P
COUNSELLORS AT LAW
28 STATE STREET
BOSTON, MASSACHUSETTS 02109-1784
TELEPHONE (617) 227-7400
FAX (617) 742-4214
www.lahive.com

UNITED STATES PATENT & TRADEMARK OFFICE
FEB 14 2005

JOHN A. LAHIVE, JR. (1928-1997)
THOMAS V. SMURZYNSKI
GIULIO A. DeCONTI, JR.
ELIZABETH A. HANLEY
AMY BAKER MANDRAGOURAS
ANTHONY A. LAURENTANO
KEVIN J. CANNING
JANE E. REMILLARD
DeANN FORAN SMITH
DEBRA J. MILASINCIC, Ph.D.
MEGAN E. WILLIAMS, Ph.D.
WILLIAM A. SCOFIELD, JR.
SIBLEY P. REPPERT
JOHN D. LANZA
DAVID R. BURNS
JOHN S. CURRAN
SEAN D. DETWEILER
HATHAWAY P. RUSSELL *
MARIA LACCOTRIPE ZACHARAKIS, Ph.D.

DANIELLE L. HERRITT
EUIHOON LEE **
MANEESH GULATI
CYNTHIA M. SOROOS
PETER W. DINI, Ph.D.
MICHAEL J. BASTIAN, Ph.D.
CHRISTOPHER J. McKENNA
VINCENT P. LOCCISANO
JAMES M. McKENZIE
CYNTHIA M. GILBERT
LEIGH J. MARTINSON

SENIOR COUNSEL
JAMES E. COCKFIELD

OF COUNSEL
JEREMIAH LYNCH
JEANNE M. DIGIORGIO
CYNTHIA L. KANIK, Ph.D.

PATENT AGENTS
JONATHAN M. SPARKS, Ph.D.
CRISTIN E. HOWLEY, Ph.D.
JILL ANN MELLO, Ph.D.
CHRISTOPHER E. DRABIK
JAMES H. VELEMA
JACOB G. WEINTRAUB

TECHNICAL SPECIALISTS
CATHERINE M. BISHOP
DEBORAH L. NAGLE, Ph.D.
ANNE JACQUELINE WIZEMAN, Ph.D.
BRIAN C. TRINQUE, Ph.D.
CHRISTOPHER R. COWLES, Ph.D.
W. ELANA WANG
MEAGHAN L. RICHMOND, Ph.D.

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February 10, 2005

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Re: U.S. Patent Application No.: 10/040,709
For: *AUTOMATED APPROACH FOR MEASURING SIGNALING SETUP,
HOLD AND JITTER*
Inventors: William B. Gist, *et al.*
Filed: January 7, 2002
Our Ref. No.: SMQ-044

Dear Sir:

I enclose herewith for filing in the above-identified application the following:

1. Information Disclosure Statement;
2. PTO Form SB/08;
3. Copies of references cited in PTO Form SB/08 (2);
4. A Return Postcard.

No additional costs are believed to be due in connection with the filing of this Information Disclosure Statement. However, please charge any necessary fees in connection with the enclosed statement to our Deposit Order Account No. 12-0080. For this purpose, a duplicate of this sheet is attached.

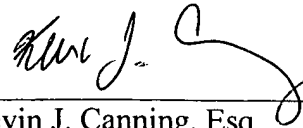
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MS Amendment, Commissioner for Patents, P.O. Box 1450,
Alexandria, Virginia 22313-1450 on:

February 10, 2005

Date

Kevin J. Canning, Esq. Registration No. 35,470

Respectfully submitted,
LAHIVE & COCKFIELD, LLP


Kevin J. Canning, Esq.
Registration No. 35,470
Attorney for Applicants



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of: William B. Gist, *et al.*

Serial No.: 10/040,709

Filed: January 7, 2002

For: *AUTOMATED APPROACH FOR MEASURING
SIGNALING SETUP, HOLD AND JITTER*

Attorney Docket No.: SMQ-044

Group Art Unit: 2123

Examiner: Not Yet Assigned

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

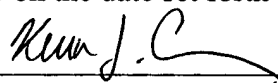
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February 10, 2005

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By:


Kevin J. Canning, Esq.
Registration No. 35,470
Attorney for Applicants

INFORMATION DISCLOSURE STATEMENT

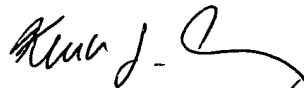
Dear Sir:

Applicants and their Attorney are aware of the following publications and information, listed on the attached PTO Form SB/08, and in accordance with 37 CFR §1.97 hereby submit these publications for the Examiner's consideration. A copy of each cited publication is enclosed.

This statement is not to be interpreted as a representation that the cited publications are material, that an exhaustive search has been conducted, or that no other relevant information exists. Nor shall the citation of any publication herein be construed *per se* as a representation that such publication is prior art. Moreover, Applicants understand that the Examiner will make an independent evaluation of the cited publications.

Under 37 CFR § 1.97(b)(3), no additional costs are believed to be due in connection with the filing of this disclosure. If, however, a first Office Action on the merits issues in this application bearing a mailing date prior to the date of this Information Disclosure Statement, please charge the appropriate fee as required under 37 CFR §1.17(p) to our Deposit Order Account No. 12-0080.

Respectfully submitted,
LAHIVE & COCKFIELD, LLP



Kevin J. Canning, Esq.
Registration No. 35,470
Attorney for Applicants

28 State Street
Boston, MA 02109
(617) 227-7400

Date: February 10, 2005

KJC/les
Enclosures



PTO/SB/08a/b (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449A/B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
				Application Number	10/040,709
				Filing Date	January 7, 2002
				First Named Inventor	William B. Gist
				Art Unit	2123
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	SMQ-044

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS						
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	A1	SepcetraQuest Signal Explorer – Pre and Post Route Signal Integrity Analysis. 2000, Cadence Design Systems, Inc., 3 pages.	
	A2	SpecetraQuest SI Expert – High Speed System Level Design and Analysis. 2000, Cadence Design Systems, Inc., 11 pages.	

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¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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